

1	ONOFF	LVDS_D0_P	128	6	GND	VDD_1V8	7
2	POR_B	LVDS_D0_N	129	8	GND	VDD_1V8	11
3	PMIC_ON_REQ	LVDS_D1_P	130	10	GND	VDD_1V8	13
4	BOOT_MODE0	LVDS_D1_N	131	12	GND	VDD_1V8	87
5	BOOT_MODE1	LVDS_D2_P	132	14	GND	VDD_1V8	88
		LVDS_D2_N	133	16	GND	VDD_1V8	89
		LVDS_D3_P	134	36	GND	VDD_1V8	90
		LVDS_D3_N	135	96	GND	VDD_1V8	91
23	GND	LVDS_D4_P	136	97	GND	VDD_1V8	92
24	USB_OTG2_VBUS	LVDS_D4_N	137	98	GND	VDD_1V8	93
25	USB_OTG1_VBUS	LVDS_D5_P	46	99	GND	VDD_1V8	94
26	GND	LVDS_D5_N	47	100	GND	VDD_1V8	95
27	USB_OTG2_DP	LVDS_D6_P	48	101	GND		
28	USB_OTG2_DN	LVDS_D6_N	49	102	GND		
		LVDS_D7_P	50	103	GND		
29	GND	LVDS_D7_N	51	104	GND		
30	USB_OTG1_DN	GND	52	113	GND	VDD_3V3	9
31	USB_OTG1_DP			114	GND	VDD_3V3	15
		UART1_RTS	53	115	GND	VDD_3V3	38
32	GND	UART1_CTS	54	116	GND	VDD_3V3	39
33	CLK1_N	UART2_TXD	55	117	GND	VDD_3V3	37
34	CLK1_P	UART2_RXD	56	118	GND	VDD_3V3	105
35	GND	UART2_RTS	57	119	GND	VDD_3V3	106
		UART2_CTS	58	120	GND	VDD_3V3	107
		GND	59	121	GND	VDD_3V3	108
40	GPIO1_IO00	GND	69	122	GND	VDD_3V3	109
41	GPIO1_IO01	GND	70	123	GND	VDD_3V3	110
42	GPIO1_IO02	GND	71	124	GND	VDD_3V3	111
43	GPIO1_IO03	GND	72	125	GND	VDD_3V3	112
44	GPIO1_IO04	eMMC_CMD	73	126	GND		
45	GPIO1_IO05	GND	74	127	GND		
		eMMC_CLK	75	138	GND		
60	SPI1_MISO	GND	76	139	GND		
61	SPI1_MOSI	eMMC_RST	77	140	GND		
62	SPI1_CLK	eMMC_DS	78				
63	SPI1_CS	eMMC_DATA_0	79				
64	SPI2_MISO	eMMC_DATA_1	80				
65	SPI2_MOSI	eMMC_DATA_2	81				
66	SPI2_CLK	eMMC_DATA_3	82				
67	SPI2_CS	eMMC_DATA_4	83				
68	GND	eMMC_DATA_5	84				
		eMMC_DATA_6	85				
17	JTAG_TCK	eMMC_DATA_7	86				
18	JTAG_TDI	GND					
19	JTAG_NTRST						
20	JTAG_TMS						
21	JTAG_TDO						
22	JTAG_MOD						