

# TIE 2018 Program

Wednesday, April 18

	<i>“Students” Track</i>	<i>“Technical &amp; Industrial Committees” Track</i>	<i>“Steering Committee” Track</i>
<b>13:00-15:00</b>	Opening of Onsite Registrations for <b>TIE &amp; TIE<sup>Plus</sup></b> – <b>Main entrance @ UPIT, Pitesti</b>		
<b>15:00-15:30</b>	<b>TIE’s first edition Boot camp: “Layout Engineering - Sharpen your skills, fast” &amp; TIE<sup>Plus</sup> preparations Room T213 @ UPIT, Pitesti</b>	Transport with organizer bus to Draexlmaier Departure from University parking	
<b>15:30-17:30</b>		Industry - academia experience exchange – <b>Draexlmaier R&amp;D and Manufacturing visit</b>	
<b>17:30-18:00</b>		Transport with organizer bus to University Departure from Draexlmaier parking	
<b>18:00-19:30</b>		<b>TIE<sup>Plus</sup> Technical meeting</b> Room T211 @ UPIT, Pitesti	<b>Steering, Industrial and Technical Committees meeting</b> Senate Room @ UPIT, Pitesti
<b>19:30-21:30</b>	<b>Welcome Dinner</b>		

Note: Items in the program marked with **bold** type represent **compulsory activities** for the given track.

Thursday, April 19

	<i>“Students” Track</i>	<i>“Technical &amp; Industrial Committees” Track</i>	<i>“Steering Committee” Track</i>
07:15-07:45	<b>Breakfast</b> Cafeteria B building		
07:45-08:00	<b>Registration of the participants</b> Main entrance @ UPIT, Pitesti		
08:00-08:15	<b>Welcome speech</b> Senate Room @ UPIT, Pitesti		
08:15-09:45	<b>TIE<sup>Plus</sup> CONTEST - 1<sup>st</sup> part</b> Senate Room @ UPIT, Pitesti		
09:45-10:00	<b>Coffee Break</b>		
10:00-12:00	<b>TIE<sup>Plus</sup> CONTEST 2<sup>nd</sup> part</b> Senate Room @ UPIT, Pitesti		
12:00-13:00	<b>TIE<sup>Plus</sup> Networking and Show-room</b> Central Aula @ UPIT, Pitesti	<b>TIE<sup>Plus</sup> Technical meeting</b> Room T211 @ UPIT, Pitesti	<b>TIE<sup>Plus</sup> Networking and Show-room visit</b>
13:00-14:00	<b>Lunch Academica</b>		
14:00-14:30	<b>TIE<sup>Plus</sup> – AWARDING CEREMONY</b> Senate Room @ UPIT, Pitesti		
14:30-16:15	<b>Technical Workshop:</b> <b>"Virtual Prototyping, Design, Validation and Manufacturing of High-End Products"</b> <b>- First Session -</b> Senate Room @ UPIT, Pitesti		
16:15-16:45	<b>Coffee Break</b>		

Thursday, April 19

<p>16:45-18:30</p>	<p><b>Technical Workshop</b> <b>"Virtual Prototyping, Design, Validation and Manufacturing of High-End Products"</b> <b>- Second Session -</b> Senate Room @ UPIT, Pitesti</p>	
<p>18:30-19:30</p>	<p><b>Dinner Academica</b></p>	
<p>20:00-21:30</p>	<p><b>TIE contest technical preparation</b> Central Aula @ UPIT, Pitesti</p>	<p><b>Steering committee meeting</b> Senate Room @ UPIT, Pitesti</p>

Friday, April 20

	<i>“Students” Track</i>	<i>“Technical &amp; Industrial Committees” Track</i>	<i>“Steering Committee” Track</i>
06:30-07:15	<b>Breakfast</b> Cafeteria B building		
07:20-7:45	Transport to TIE contest		
07:45-08:15	TIE contest preliminary activities		
08:15-12:15	<b>TIE CONTEST</b> Central Aula @ UPIT, Pitesti	<b>Technical session</b> Room T211 @ UPIT, Pitesti	<b>WORKSHOP for: “Strategic partnership for education”</b> Senate Room @ UPIT, Pitesti
12:30-13:30	<b>Lunch Academica</b>		
13:30-17:30	<b>Assessment of the projects; litigations</b> Central Aula @ UPIT, Pitesti		<b>WORKSHOP “Strategic partnership for education”</b> (includes assessment participation) Senate Room @ UPIT, Pitesti
17:30-18:30	Networking and Show-room visit	<b>TIE 2018 Result Evaluation</b> Room T211 @ UPIT, Pitesti	
18:30-20:30	<b>TIE - AWARDING CEREMONY</b> – Senate Room @ UPIT, Pitesti		
20:30-22:30	<b>Gala dinner TIE 2018 - Academica</b>		