

TIE 2017 Program

Tuesday, April 25 – 2017

	<i>“Students” Track</i>	<i>“Technical & Industrial Committees” Track</i>	<i>“Steering Committee” Track</i>
14:00-16:00	Opening of Onsite Registrations for TIE&TIEplus – Sala Pasilor Pierduti, TUIASI, Building A		
16:00-18:30	TIEplus preparations TUIASI P4, Building A	Industry - academia experience exchange - TUIASI, Building A Microwave Laboratory / Continental Laboratory visit	
18:30-20:00		TIEplus Technical meeting Council Room, TUIASI, Building A	Steering, Industrial and Technical Committees meeting Room P6, TUIASI Building A
20:00-20:30	Dinner Student cafeteria Tudor Vladimirescu	Transport to Water Tower Restaurant	
20:00-21:00		Dinner Water Tower Restaurant	

Wednesday, April 26 – 2017, first half of day

	<i>“Students” Track</i>	<i>“Technical & Industrial Committees” Track</i>	<i>“Steering Committee” Track</i>
06:45-07:15	Breakfast Student cafeteria Tudor Vladimirescu	Breakfast at accomodation	
07:15-07:45	Transport to Bd. Carol I nr.11A, Iași		
07:45-13:00	Registration of the participants - “Sala Pasilor Pierduti”, TUIASI Building A		
08:00-08:05	Welcome speech - Aula TUIASI, Building A		
08:05-09:30	TIEplus 2017 CONTEST - 1st part Aula TUIASI, Building A		
09:30-09:45	Coffee Break - Sala Pasilor Pierduti, TUIASI Building A		
09:45-11:30	TIEplus 2017 CONTEST 2nd part - Aula TUIASI, Building A		Industries, Academia & State WORKSHOP for: “Strategic partnership for education” Room P4, Building A
11:30-13:15	TIEplus experience exchange Contest show-room visit	TIEplus 2017 Technical meeting Aula TUIASI, Building A	
13:15-13:30	TIEplus 2017 – AWARDING CEREMONY Aula TUIASI, Building A		
13:30-14:30	Lunch: Sala Pasilor Pierduti, TUIASI Building A		

Wednesday, April 26 – 2017, second half of day

14:30-16:15	Technical Workshop - Aula TUIASI, Building A “TIEplus. The step towards interconnect simulation technology” – Catalin NEGREA, Continental Automotive Romania “Solution for simulation of RF, SI/PI and EMI/EMC application” – Alain MICHEL, ANSYS France “Thermal management aspects in electronic design” – Aurelian BOTAU/Gabriel CIOBANU, Continental Automotive Romania		
16:15-16:30	Coffee Break - Sala Pasilor Pierduti, TUIASI Building A		
16:30-18:30	Technical Workshop - Aula TUIASI, Building A “Modeling, design and optimization of electronic package designs” – Danilo Di FEBBO, CST AG, Treviglio, Italy “PCB and Stencil Design Correlation dedicated to Pin-in-Paste Technology” – Ioan PLOTOG, UPB-CETTI “Integrated Project for Bachelor Students-a Key Action for Success in Education” - Mihaela PANTAZICĂ, Gaudențiu VÂRZARU, UPB-CETTI		
18:30-19:30	TIE Student Technical Session Aula TUIASI, Building A	TIE Technical meeting Room P4, TUIASI, Building A	Steering committee meeting Council Room, Building A
19:30-20:30	Dinner - Sala Pasilor Pierduti, TUIASI Building A		
20:30-21:00	TIE Student ECAD preparation TUIASI Library, Building A	TIE Technical meeting Room P4, TUIASI, Building A	Steering committee meeting Council Room, Building A

Thursday, April 27 – 2017

	<i>“Students” Track</i>	<i>“Technical & Industrial Committees” Track</i>	<i>“Steering Committee” Track</i>
06:00-06:45	Breakfast Student cafeteria Tudor Vladimirescu		
06:45-07:15	Transport to Bd. Carol I nr.11A, Iași		
07:30-08:00	TIE 2017 contest preliminary activities TUIASI Library, Building A		
08:00-12:00	TIE 2017 CONTEST TUIASI Library, Building A	Technical session Room P8, TUIASI, Building A	Steering committee contest observation
12:00-13:00	Lunch Sala Pasilor Pierduti, TUIASI Building A		
13:00-18:00	Assessment of the TIE 2017 projects; litigations TUIASI Library, Building A		Steering committee meeting Council Room, Building A
18:00-19:00	TIE experience exchange Contest show-room visit	TIE 2017 Result Evaluation Room P4, TUIASI, Building A	
19:00-20:00	TIE 2017 - AWARDING CEREMONY - Aula TUIASI, Building A		
20:00-22:00	Gala dinner TIE 2017 Sala Pasilor Pierduti, TUIASI Building A		

Friday, April 28 – 2017

08:00-09:00

Breakfast

09:00-09:30

Transport to Bd. Poitiers nr. 6, 700671 Iași

09:30-11:30

Industry - academia experience exchange - Bd. Poitiers nr. 6,
700671 Iași

RD tour @ Continental Automotive Romania

11:30-12:00

Ending session / Final remarks – Meeting Room @ Continental
Automotive

Lunch and Departure

Note: Items in the program marked with **bold** type represent **compulsory activities** for the given track.

TIE 2017
DESIGN OF ELECTRONIC
MODULES AND ASSEMBLIES
Student Professional Contest

Awarding Ceremony

April 27, 2017 - Aula TUIASI, Building A

- 19:00-19:10** ***Opening Ceremony Speech:***
Prof. Daniela Tărniceriu, Ph.D., Dean of Faculty of Electronics, Telecommunications and Information Technology Gh. Asachi Technical University, Iași
- 19:10-19:20** ***State of the art TIE 2017***
Prof. Norocel Codreanu, Ph.D., Politehnica University of Bucharest,
TIE Technical Committee Chair
- 19:20-19:30** ***TIE 2017 Awarding***
Prof. Dr.h.c.mult. Paul Svasta, Ph.D., Politehnica University of Bucharest,
TIE General Chair
Prof. Dan Pitică, Ph.D., Technical University of Cluj Napoca,
TIE General Academic Co-Chair
- 19:40-19:50** ***PCB Designer Certification recommended by TIE Industrial Committee***
Dipl. Eng. Cosmin Moisă, Continental Automotive Timișoara,
TIE General Industrial Co-Chair
Assoc. Prof. Gabriel Chindriș, Ph.D., Technical University of Cluj Napoca,
TIE Industrial Advisor Committee, Academic Co-Chair
- 19:50-20:00** ***Looking Forward TIE 2018***
Prof. Nicu Bizon, Ph.D., Dean of Faculty of Electronics, Communications and Computers, University of Pitești,
TIE 2018 Chair

TIE 2017 Gala Dinner (Sala Pașilor Pierduti, TUIASI Building A)